

transmit traffic channel data during the absence of signals from the base stations. This distinction is indicated in the specification of Tanaka starting at column 3, line 2, and shown in Figure 3, both of which concern the superframe format of a logical control channel (LCCH). The specification discloses that each intermittent transmission slot consists of 20 TDMA frames, each having a period of 5 msec., and that the first slot is assigned to the LCCH. Figure 3 illustrates that the intermittent transmission slot assigned to the LCCH is time slot #T1. As a result, the mobile terminals can receive control signals from the base station at intervals equal to 100 msec. Because of this, and of Tanaka's TDMA nature, data of a traffic channel cannot be transmitted during the timing to which the LCCH is assigned.

A further illustration of the transmission of traffic channel data only during the absence of signals from the base station can be found in the paragraph beginning at column 6, line 6, describing the intermittent receiving and transmitting modes of Figures 11 and 12. The specification, using an example in which time slot #T2 is used, notes that the power-supplying controller supplies power to the receiver only at the timing of the time slot, #R2, and additionally supplies power to the transmitter supply only at the timing of the time slot #T2. Therefore, traffic channel data cannot be transmitted during time slots #T1, #T2, or during any time slot in which the LCCH is assigned.

In view of the above distinctions, Applicant respectfully submits that the present invention can neither be anticipated nor rendered obvious by the cited prior art references. Reconsideration is respectfully requested.

Additionally, Applicant respectfully requests that the application be allowed.

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Respectfully submitted,

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